

Semiconductor working group of ETG celebrates its tenth anniversary

Recently the 20th meeting of the semiconductor working group of the ETG, the so-called TWG Semi, took place. At the digital meeting, however, not only work was done, but, as an exception to the rule, also celebrations were held. It was already ten years ago that the working group was launched at an official kick-off meeting. At that time, almost 100 industry experts were present who agreed to support the ETG in the project to develop device profiles and implementation guidelines for the special requirements of the semiconductor manufacturing industry. Martin Rostan, Executive Director of the ETG, recognized even back then: "The new fieldbus standard for the semiconductor industry is EtherCAT – there is no other way to interpret the clear statements of the market leaders and the active support of the industry." Ten years later, TWG Semi looks back on a large number of so-called SDPs (Specific Device Profiles), i.e. documents with device profiles for the semiconductor manufacturing industry. In addition, new areas are constantly finding their way into the group's work. For example, the group now deals with topics such as Safety over EtherCAT and its relevance in the area of functional safety for semiconductor manufacturing machines and the associated devices. In addition, the integration of the EtherCAT Conformance Test Tool (CTT) in the automated acceptance of devices also plays a role. Florian Essler, who leads the working group as ETG's EtherCAT expert, is sure to have "actively tackled" the start for the next ten years with the 20th meeting of the TWG Semi and is looking forward to future ideas, which will be developed and elaborated in addition to the usual daily business of the working group.



At the kick-off meeting of the Semi TWG in 2011, nearly 100 participants were present. Now the working group celebrated its tenth anniversary at its 20th meeting (online).